

# GL10-SM31LR10x

#### SFP+ 10Gb/s 1310nm 10km Transceiver

#### **PRODUCT FEATURES**

- Supports up to 11.3Gbps bit rates
- Hot-plug-gable SFP+ footprint
- Up to 10km for SMF
- DBF laser and PIN photo-diode,
- Compliant with SFP+ MSA and SFF-8472
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- RoHS compliant
- Operating case temperature:

Commercial: 0 to +70°C Industrial: -40 to +85°C

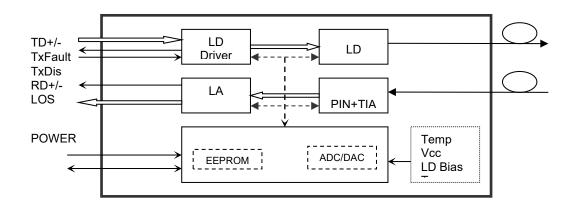
#### **APPLICATIONS**

- 10Gbps Optical systems
- 10GBASE-LR at 10.3125Gbps
- 10GBASE-LW at 9.953Gbps
- LTE systems

#### PRODUCT DESCRIPTION

The GL10-SM31LR10x SFP+ transceivers are high performance, cost effective modules supporting data rate of 11.3Gbps and 10km transmission distance with SMF. The transceiver consists of three sections: a DFB laser transmitter, a PIN photo-diode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.





### **Ordering information**

Product part Number	Data Rate (Gbps)	Media	Wavelength (nm)	Transmission Distance(km)	Temperatu T <sub>cas</sub>	ire Range e / °C
GL10-SM31LR10C	10.3	SMF	1310	10	0~70	Commercial
GL10-SM31LR10T	10.3	SMF	1310	10	-40~85	Industrial

### **Absolute Maximum Ratings**

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

## **Recommended Operating Conditions**

Parameter	Symbol	Min	Тур	Max	Unit
Casa Operating Temperature		0	-	70	
Case Operating Temperature Range	Тс	-40	-	85	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			350	mA
Data Rate			10.3125	11.3	Gbps



## **Optical and Electrical Characteristics**

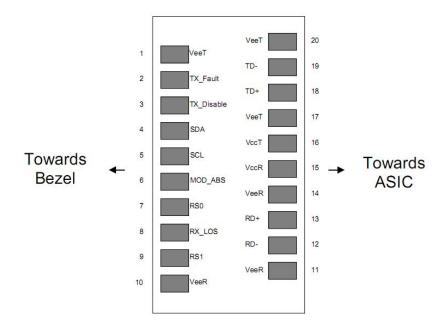
Parameter		Symbol	Min	Typical	Max	Unit	Notes
	Transmitter						
Centre V	Vavelength	λο		1310		nm	
Spectral Wi	dth (-20dB)	Δλ			1	nm	
Side-Mode Si	appression Ratio	SMSR	30	-		dB	
Average (	Output Power	Pout	-6.5		+0.5	dBm	1
Extinct	tion Ratio	ER	3.5			dB	
Data Input Sv	Data Input Swing Differential		180		850	mV	2
Input Differe	ntial Impedance	Z <sub>IN</sub>	90	100	110	Ω	
TV D' 11	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
TV F 1	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receive	er		<u>'</u>	
Centre V	Centre Wavelength		1260	1310	1610	nm	
Receiver	Receiver Sensitivity				-14.4	dBm	3
Receive	Receiver Overload		0.5			dBm	3
LOS De-Assert		LOSD			-17	dBm	
LOS Assert		LOSA	-26			dBm	
LOS Hysteresis			0.5			dB	
Data Output Swing Differential		V <sub>out</sub>	300		900	mV	4
	LOS		2.0		Vcc	V	
					0.8	V	

#### Notes:

- The optical power is launched into SMF.
  PECL input, internally AC-coupled and terminated.
  Measured with a PRBS 2<sup>31</sup>-1 test pattern @10312Mbps, BER ≤1×10<sup>-12</sup>.
- 4. Internally AC-coupled.



## **Pin Description**



Pin	Signal Name	Description	Plug Seq.	Notes
1	V <sub>EET</sub>	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V <sub>EER</sub>	Receiver ground	1	
11	V <sub>EER</sub>	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V <sub>EER</sub>	Receiver ground	1	
15	V <sub>CCR</sub>	Receiver Power Supply	2	
16	V <sub>CCT</sub>	Transmitter Power Supply	2	
17	V <sub>EET</sub>	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V <sub>EET</sub>	Transmitter Ground	1	

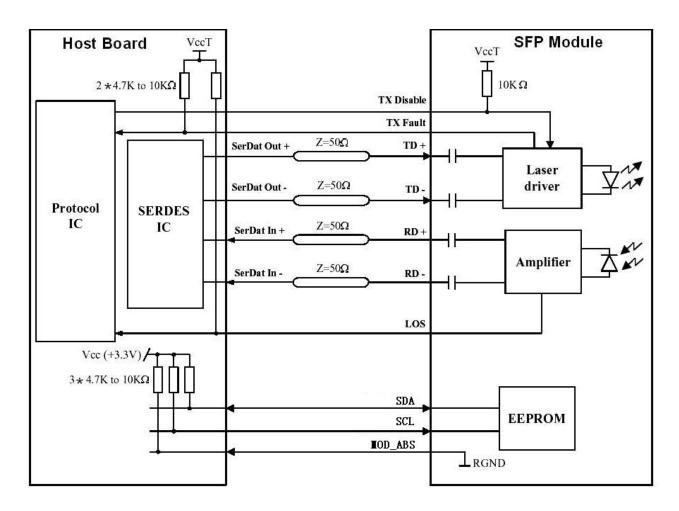


#### Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

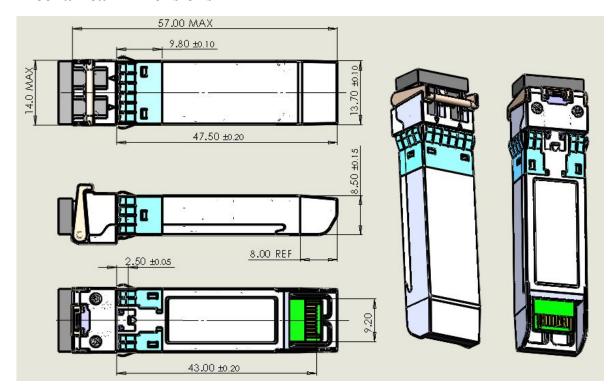
- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with  $100\Omega$  (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

#### **Recommended Interface Circuit**





### **Mechanical Dimensions**



### **Regulatory Compliance**

Feature	Reference	Performance	
Electrostatic discharge (ESD)	IEC/EN 61000-4-2	Compatible with standards	
Electromagnetic Interference	FCC Part 15 Class B EN 55022	Compatible with standards	
(EMI)	Class B (CISPR 22A)		
Laser Eye Safety	IEC/EN 60825-1, 2	Class 1 laser product	
ROHS	2002/95/EC	Compatible with standards	
EMC	EN61000-3	Compatible with standards	